

What is claimed is:

1. A control circuit that feeds a control signal based on an input signal to an image signal line driving circuit and a scan line driving circuit both for driving a liquid crystal panel;

the control circuit comprising:

a control signal generating section that generates a control signal containing a start pulse for starting operation of said scan line driving circuit; and

a protection circuit that normalizes said start pulse generated by said control signal generating section in response to said input signal even when said inputted signal is not normal.

2. The control circuit according to claim 1, wherein said protection circuit comprises:

a counter for counting up to a predetermined count number based on a clock inputted;

a comparator arranged at the subsequent stage of said counter to determine whether or not said counter is under counting; and

a logic element arranged at the subsequent stage of said comparator to output a start pulse normalized by logical operation of a start pulse generated by said control signal generating section and an output signal of said comparator.

3. The control circuit according to claim 1, wherein said control signal generating section and said protection circuit are integrated into a semiconductor device.

4. A liquid crystal display comprising:

the control circuit as defined in claim 1;
an image signal line driving circuit and a scan line driving circuit both for outputting a drive signal in response to a control signal fed by said control circuit; and
a liquid crystal display for displaying an image, which is driven by said image signal line driving circuit and said scan line driving circuit.